

```

1  __global__ void
2  culsp_kernel(float *d_t, float *d_X, float *d_P,
3              float df, int N_t)
4  {
5
6      __shared__ float s_t[BLOCK_SIZE];
7      __shared__ float s_X[BLOCK_SIZE];
8
9      // Calculate the frequency
10
11     float f = (blockIdx.x*BLOCK_SIZE+threadIdx.x+1)*df;
12
13     // Calculate the various sums
14
15     float XC = 0.f;
16     float XS = 0.f;
17     float CC = 0.f;
18     float CS = 0.f;
19
20     float XC_chunk = 0.f;
21     float XS_chunk = 0.f;
22     float CC_chunk = 0.f;
23     float CS_chunk = 0.f;
24
25     int j;
26
27     for(j = 0; j < N_t; j += BLOCK_SIZE) {
28
29         // Load the chunk into shared memory
30
31         __syncthreads();
32
33         s_t[threadIdx.x] = d_t[j+threadIdx.x];
34         s_X[threadIdx.x] = d_X[j+threadIdx.x];
35
36         __syncthreads();
37
38         // Update the sums
39
40         #pragma unroll
41         for(int k = 0; k < BLOCK_SIZE; k++) {
42
43             float ft = f*s_t[k];
44             ft -= (int) ft;
45

```

```

46         float c;
47         float s;
48
49         __sincosf(TWOPI*ft, &s, &c);
50
51         XC_chunk += s_X[k]*c;
52         XS_chunk += s_X[k]*s;
53         CC_chunk += c*c;
54         CS_chunk += c*s;
55
56     }
57
58     XC += XC_chunk;
59     XS += XS_chunk;
60     CC += CC_chunk;
61     CS += CS_chunk;
62
63     XC_chunk = 0.f;
64     XS_chunk = 0.f;
65     CC_chunk = 0.f;
66     CS_chunk = 0.f;
67
68 }
69
70 float SS = (float) N_t - CC;
71
72 // Calculate the tau terms
73
74 float ct;
75 float st;
76
77 __sincosf(0.5f*atan2(2.f*CS, CC-SS), &st, &ct);
78
79 // Calculate P
80
81 d_P[blockIdx.x*BLOCK_SIZE+threadIdx.x] =
82     0.5f*((ct*XC + st*XS)*(ct*XC + st*XS)/
83         (ct*ct*CC + 2*ct*st*CS + st*st*SS) +
84         (ct*XS - st*XC)*(ct*XS - st*XC)/
85         (ct*ct*SS - 2*ct*st*CS + st*st*CC));
86
87 // Finish
88
89 }
90

```

FIG. 1.— Abridged source for the CULSP computation kernel.